

IN THE CLAIMS

- 1 1. (Canceled)
- 1 2. (Previously Presented) The arrangement of claim 22, wherein said common bus is a CAN-  
2 bus.
- 1 3. (Previously Presented) The arrangement of claim 22, wherein said input member comprises a  
2 control unit, a bus controller, bus driver, memory unit and an input signal controller.
- 1 4. (Original) The arrangement of claim 3, wherein the controller unit consists of a  
2 microprocessor or other data processing arrangement.
- 1 5. (Original) The arrangement of claim 3, wherein said bus controller is a CAN-bus controller  
2 and the bus driver is a CAN-bus driver.
- 1 6. (Previously Presented) The arrangement of claim 22, wherein said output member comprises  
2 a control unit, a bus controller, bus driver, memory unit and an output signal controller.
- 1 7. (Original) The arrangement of claim 6, wherein the controller unit consists of a  
2 microprocessor or other data processing arrangement.
- 1 8. (Original) The arrangement of claim 6, wherein said bus controller is a CAN-bus controller  
2 and the bus driver is a CAN-bus driver.
- 1 9. (Previously Presented) The arrangement of claim 22, wherein said at least one contact of the  
2 input member is so provided that they alter between an input and output contact state.

1 10. (Previously Presented) The arrangement of claim 9, wherein said input/output member  
2 comprises a number of input/output contacts and at least one of said input/output contacts is  
3 arranged as a common signal contact.

1 11. (Previously Presented) The arrangement of claim 10, wherein said common signal has  
2 different states, determining different states for said input contact.

1 12. (Previously Presented) The arrangement of claim 9, wherein each input contact is connected  
2 to a normally open or normally closed switch, which state is determined by programming a  
3 control unit.

1 13. (Previously Presented) The arrangement of claim 9, wherein said states of said input contact  
2 are determined to bistable or pulse.

1 14. (Previously Presented) The arrangement of claim 22, wherein said at least one contact of  
2 output member is arranged such that it allows tuning a current limitation for said at least one  
3 output contact.

1 15. (Previously Presented) The arrangement of claim 22, wherein said input and output  
2 members are connected to a common power supply line.

1 16. (Original) The arrangement of claim 15, wherein said common power supply line is  
2 arranged as a loop.

1 17. (Original) The arrangement of claim 15, wherein said common power supply line is  
2 connected to a power supply arrangement at each end.1

1 18. (Original) The arrangement of claim 17, wherein said common power supply line is  
2 provided with an arrangement for detecting an excess-current.

1 19. (Previously Presented) The arrangement of claim 22, wherein said common bus is used to  
2 communicate control commands and status messages between said input and output members.

1 20. (Previously Presented) The arrangement of claim 22, wherein input members are connected  
2 to switch and indicator groups, which are interconnected by means of a common signal line.

1 21. (Currently Amended) A method of controlling a plurality of controllable devices  
2 interconnected through at least one common signaling bus, the method comprising:

3         arranging at least one input member and at least one output member associated with a  
4 node, each input and output member having at least one input contact and at least one output  
5 contact and each contact having a unique identity and each input and output member  
6 communicating through said common bus,

7         arranging said input member to receive a control signal from at least one control  
8 arrangement connected to said input contact of said input member,

9         upon reception of said control signal generating an action signal comprising an  
10 address corresponding to a unique identity of said output contact connected to at least one of  
11 said controllable devices, and

12         providing said action signal on said common bus by said input member to be received  
13 by said output member connected to at least one of said controllable devices.

1 22. (Currently Amended) A network arrangement comprising:

2 at least one common bus,  
3 an input member connected to said common bus associated with a processing node,  
4 an output member connected to said common bus associated with said processing node,  
5 said input member comprises at least one input contact defined with a unique identity,  
6 said output member comprises at least one output contact defined with a unique identity,  
7 said input member being adapted to receive an input signal through said at least one input  
8 contact,  
9 said output member being adapted to provide an output signal through said at least one output  
10 contact,  
11 wherein said input member is arranged to generate an action signal transmitted over said buss  
12 from said input member to said output member, said action signal comprising an address  
13 corresponding to said unique identity of an output contact.